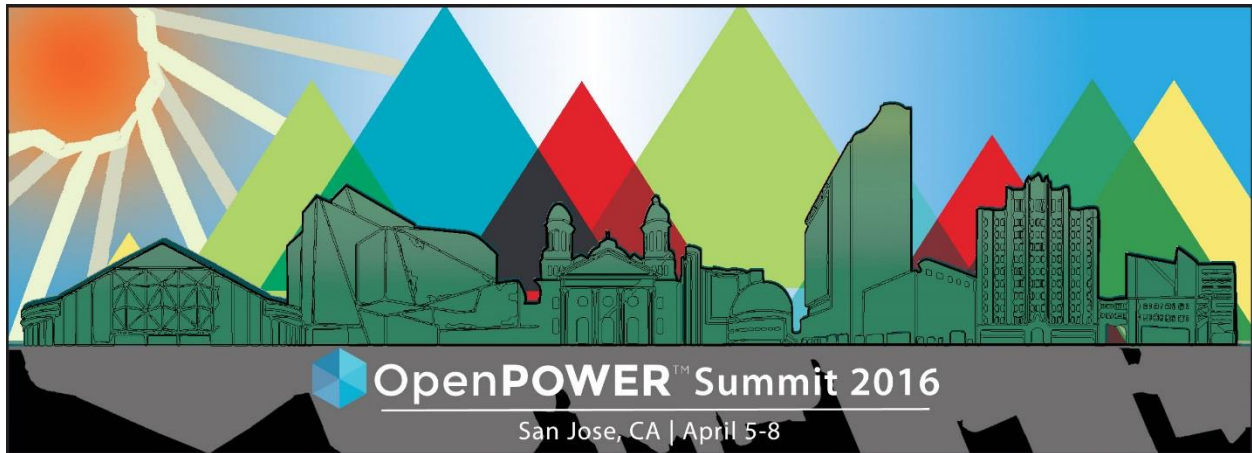


## Revolutionizing the Datacenter



**Come Join Us!**

## OpenPOWER CAPI/FPGA Lab

**Date: Friday, April 8, 2016**

**Time: 8:30 am to 11:00 am (Breakfast 8:00am to 8:30 am)**

**Location: Hilton Hotel – Santa Clara I**

Attend the OpenPOWER Summit lab and learn how the combination of IBM POWER8 processors, the IBM CAPI feature, Xilinx FPGA acceleration cards, and OpenCL can meet the computing challenges of next generation accelerated applications. This lab will provide a hands on introduction to software acceleration and optimization using the Xilinx SDAccel development environment. The SDAccel development environment leverages the industry standard APIs and programming model of OpenCL to expose FPGA based acceleration and true shared virtual memory to application programmers without requiring FPGA architecture expertise.

***Seating is limited to the first 30 OpenPOWER Members.***

*For those of you who are not an OpenPOWER member, you will be put on a waiting list.*

The Second OpenPOWER Summit will take place at the San Jose Convention Center. It will be hosted within the GPU Technology Conference (GTC) which will have thousands of technology sector attendees, including developers, researchers, and industry luminaries and significant industry press and analysts. The OpenPOWER rapidly expanding ecosystem is fueled by 175+ members worldwide working on more than 100 innovations!

The OpenPOWER Foundation members' individual and collective efforts are positively changing the game, delivering innovations that advance data center technology, expand choice and drive market efficiency.

**Sign Up Today! Contact: Joni Sterlacci [j.sterlacci@ieee.org](mailto:j.sterlacci@ieee.org)**

**Register: <http://www.gputechconf.com/attend/pricing>**

**OpenPOWER Members receive a 20% discount off of the standard GTC registration fees using code [OPFSUMMIT2016](#).**